

PTO/SB/08A (08-03)

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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

*(Use as many sheets as necessary)*

Sheet 1

of 2

**Complete if Known**

Application Number	10/812,279
Filing Date	March 29, 2004
First Named Inventor	Daniel Q. Zhu
Art Unit	2615
Examiner Name	na
Attorney Docket Number	91922-15

## U. S. PATENT DOCUMENTS

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## FOREIGN PATENT DOCUMENTS

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**Examiner  
Signature**

Date Considered

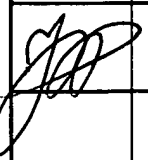
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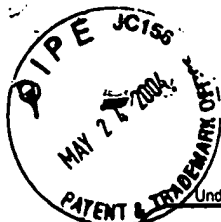
NON PATENT LITERATURE DOCUMENTS			
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		European Search Report for European Patent Application No. EP05251940; July 6, 2005; pages 1-2.	

Examiner Signature		Date Considered	12/3/07
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### U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
		US- 6,281,813	Aug 28, 2001	Vierthaler et al.	
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		Country Code <sup>4</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				

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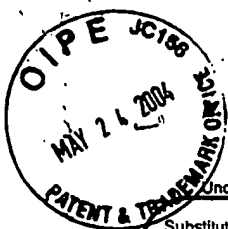
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### NON PATENT LITERATURE DOCUMENTS

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		Micronas Application Note IC "DRX 396xA PCB Layout Recommendations" Edition Nov. 7, 2002, 6251-510-2-1AN	
		Micronas Short Information DRX 3960A Digital Receiver Front-end; Oct/2000	
		Micronas Data Sheet "MSP 34x0G Multistandard Sound Processor Family" Edition May 27, 2003 6251-476-1DS	
		Philips Semiconductors Data Sheet Product specification "TDA9855 I2C-bus controlled BTSC stereo/SAP decoder and audio processor; Nov 4, 1997	

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